#### **FEATURES**

- \* Low noise recovery: Ultra soft fast recovery diode.
- \* High reverse recovery capability: Super HiRC Structure.
- \* High reliability, high durability diodes.
- \* Isolated heat sink (terminal to base).

### **ABSOLUTE MAXIMUM RATINGS** (T<sub>C</sub>=25°C)

Item		Symbol	Unit	MDM500H65E2	
Repetitive Peak Reverse Voltage $T_{vj} = 125^{\circ}C$ $T_{vj} = 25^{\circ}C$ $T_{vj} = -40^{\circ}C$				6,500	
		V <sub>RRM</sub>	V	6,500	
				6,000	
Forward Current	DC ,	I <sub>F</sub>	A	500	
	1ms	I <sub>FM</sub>		1,000	
Junction Temperature		T <sub>vj op</sub>	°C	-40 ~ +125	
Storage Temperature		$T_{stg}$	°C	-50 ~ +125	
Isolation Test Voltage	Terminals-base	$V_{ISO}$	V <sub>RMS</sub>	10,200(AC 1 minute)	
	Terminal 1-Terminal 2	$V_{\rm ISO\ T-T}$	VRMS	10,200(AC 1 minute)	
Screw Torque	Terminals (M8)	•	N⋅m	10 (1)	
	Mounting (M6)	-	111.111	6 (2)	

Notes: (1) Recommended Value 9±1N⋅m

(2) Recommended Value 5.5±0.5N·m

#### **ELECTRICAL CHARACTERISTICS**

Item	Symbol	Unit	Min.	Тур.	Max.	Test Conditions
Repetitive Reverse Current	I <sub>RRM</sub>	mΑ	-	7	50	V <sub>R</sub> =6,500V, T <sub>vj</sub> =150°C
Forward Voltage Drop	VF	V	-	3.7	-	I <sub>F</sub> =500A, T <sub>vj</sub> =25°C
Forward Voltage Drop	VF		3.7	4.1	4.6	I <sub>F</sub> =500A, T <sub>vj</sub> =125°C
Reverse Recovery Time	t <sub>rr</sub>	μS	-	0.8	-	V <sub>R</sub> =3,600V, I <sub>F</sub> =500A, L <sub>S</sub> =200nH
Payaraa Pagayaru Laga	E <sub>rr(10%)</sub>	J/P	-	1.65	2.1	
Reverse Recovery Loss	E <sub>rr(full)</sub>	J/P	-	1.8	-	$T_{vj}$ =125°C, Rg=12 $\Omega$ (3)

#### **PACKAGE CHARACTERISTICS**

Item	Symbol	Unit	Min.	Тур.	Max.	Test Conditions
Terminal Resistance	R <sub>CE</sub>	mΩ	ı	0.3	-	per arm, T <sub>vj</sub> =25°C
Stray inductance module	L <sub>SCE</sub>	nΗ	-	42	-	per arm
Thermal Impedance	R <sub>th(j-c)</sub>	K/W	-	-	0.026	Junction to case (per arm)
Comparative tracking index	CTI		-	600	-	
Contact Thermal Impedance	R <sub>th(c-f)</sub>	K/W	-	0.007	-	Case to fin

Notes: (3) Counter arm; MBN500H65E2 VGE=+/-15V

 $R_{\rm G}$  value is the test condition's value for evaluation of the switching times, not recommended value. Please, determine the suitable  $R_{\rm G}$  value after the measurement of switching waveforms (overshoot voltage, etc.) with appliance mounted

- \* Please contact our representatives at order.
- \* For improvement, specifications are subject to change without notice.
- \* For actual application, please confirm this spec sheet is the newest revision.
- \* ELECTRICAL CHARACTERISTIC items shown in above table are according to IEC 60747-2.

DIODE MODULE Spec.No.SR2-SP-09007 R7 P 2

# MDM500H65E2

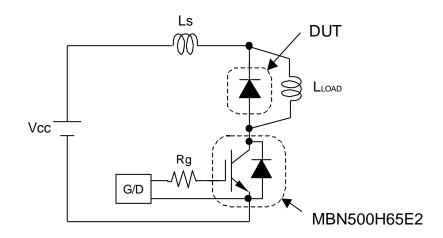


Fig.1 Switching test circuit

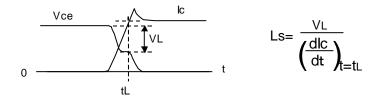


Fig.2 Definition of stray inductance

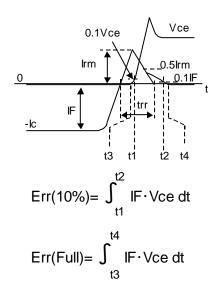
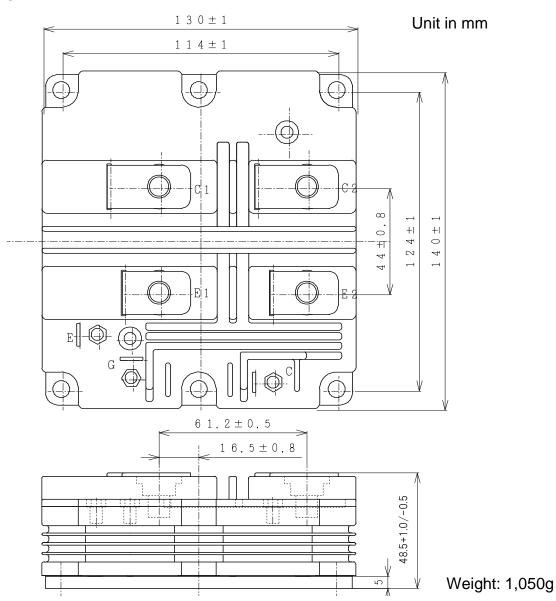
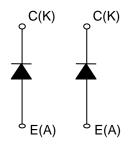


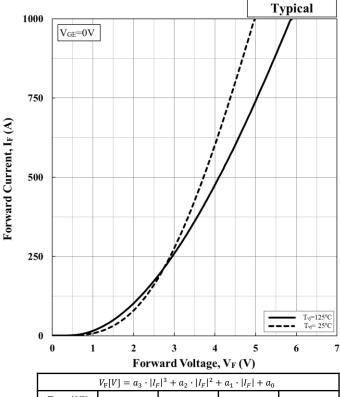
Fig.3 Definition of switching loss

### **OUTLINE DRAWING**



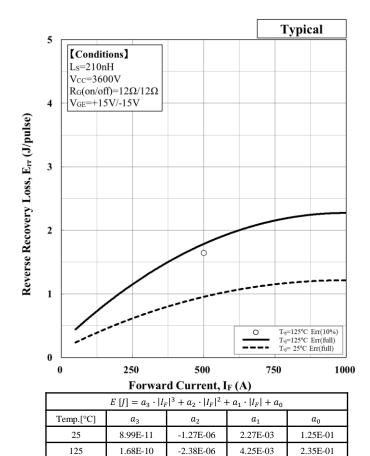
### **CIRCUIT DIAGRAM**



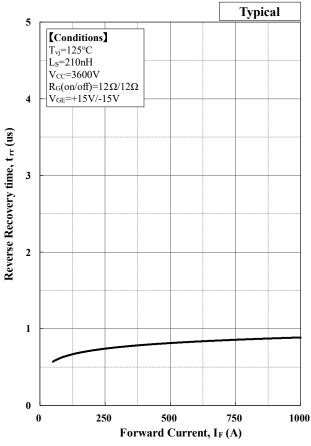


$V_{F}[V] = a_{3} \cdot  I_{F} ^{3} + a_{2} \cdot  I_{F} ^{2} + a_{1} \cdot  I_{F}  + a_{0}$						
Temp.[°C]	$a_3$	$a_2$	$a_1$	$a_0$		
25	2.93E-09	-6.34E-06	6.92E-03	1.50E+00		
125	2.89E-09	-6.62E-06	8.45E-03	1.19E+00		

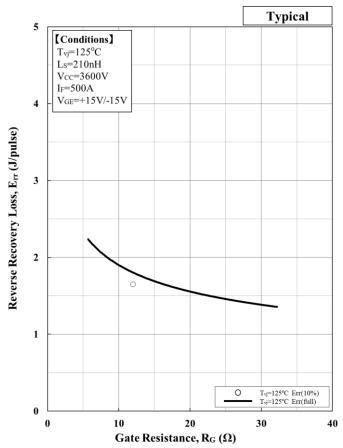
Forward Voltage of diode



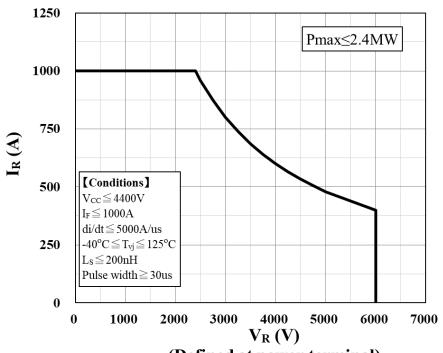
Recovery loss vs. Forward current

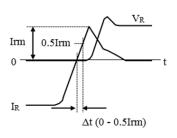


Reverse Recovery time vs. Forward Current



Reverse Recovery loss vs. Gate Resistance

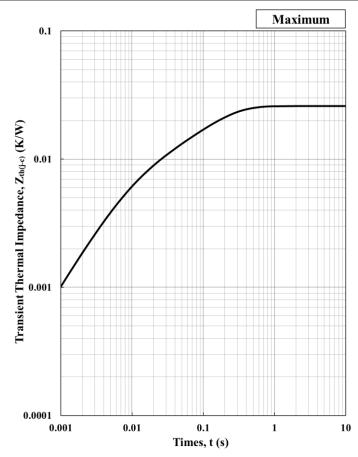




 $di/dt = \frac{0.5Irm}{\Delta t}$ 

Definition of Recovery di/dt

(Defined at power terminal) Reverse Recovery Safe Operation Area (RRSOA)



**Transient Thermal Impedance Curve** 

Foster model lumped circuit constant

n	1	2	3	4
R th, Diode [n]	1.61E-02	5.01E-03	4.65E-03	1.98E-04
C th, Diode [n]	1.02E+01	5.74E+00	1.50E+00	4.72E+00

### Cauer model lumped circuit constant

n	1	2	3	4
R th, Diode [n]	4.02E-03	5.26E-03	8.17E-03	8.52E-03
C th, Diode [n]	8.70E-01	5.02E-01	4.18E+00	1.15E+01

#### **Material declaration**

Please note the following materials are contained in the product, in order to keep characteristic and reliability level.

Material	Contained part
Lead (Pb) and its compounds	Solder

### Minebea POWER SEMICONDUCTORS

### Notices

- 1. Since mishandling of semiconductor devices may cause malfunctions, please be sure to read "Precautions for Safe Use and Notices" in the individual brochure before use.
- 2. When designing an electronic circuit using semiconductor devices, please do not exceed the absolute maximum rating specified for the device under any external fluctuations. And for pulse applications, please also do not exceed the "Safe Operating Area (SOA)".
- 3. Semiconductor devices may sometimes break down by accidental or unexpected surge voltage, so please be careful about the safety design such as redundant design and malfunction prevention design which don't cause the damage expand even if they break down.
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- 7. The information given herein, including the specifications and dimensions, is subject to change without prior notice to improve product characteristics. Before ordering, purchasers are advised to contact with Minebea power semiconductor sales department for the latest version of this data sheets
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### Minebea POWER SEMICONDUCTORS

### ■ Usage I

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