MDM900E17D

FEATURES

* Low noise due to ultra soft fast recovery diode.
* High reliability, high durability diodes.
* Isolated heat sink (terminal to base).

ABSOLUTE MAXIMUM RATINGS (T_C=25ºC)

Item		Symbol	Unit	MDM900E17D
Repetitive Peak Reverse Voltage		V_{RRM}	V	1,700
Forward Current	DC	I_F	A	900
	1ms	I_{FM}		1,800
Operating Junction Temperature		$T_{vj\ op}$	ºC	-40 ~ +125
Storage Temperature		T_{stg}	ºC	-40 ~ +125
Isolation Test Voltage	Terminals-base	V_{ISO}	V_{RMS}	4,000(AC 1 minute)
	Terminal 1-Terminal 2	$V_{ISO\ T\text{-}T}$		4,000(AC 1 minute)
Screw Torque	Terminals (M8)	-	N·m	15 (1)
	Mounting (M6)	-		6 (2)

Notes: (1) Recommended Value 15^{+0}_{-3}N·m (2) Recommended Value 5.5±0.5N·m

ELECTRICAL CHARACTERISTICS

Item	Symbol	Unit	Min.	Typ.	Max.	Test Conditions
Repetitive Reverse Current	I_{RRM}	mA	-	1.0	10.0	V_{AK}=1,700V, V_{GE}=0V, T_{vj}=125ºC
Forward Voltage Drop	V_F	V	1.5	2.0	2.5	I_F=900A, T_{vj}=125ºC
Reverse Recovery Time	t_{rr}	μs	-	0.7	1.4	V_{CC}=900V, I_F=900A, Ls=180nH
Reverse Recovery Loss	$E_{rr(10\%)}$	J/P	-	0.4	0.7	R_G=1.5Ω, T_{vj}=125ºC (3)(Fig.1) (Type test)
Reverse Recovery Time(2)	$t_{rr}(2)$	μs	-	0.7	1.4	V_{CC}=900V, I_F=1800A, Ls=80nH
Reverse Recovery Loss(2)	$E_{rr(full)}(2)$	J/P	-	0.8	1.4	R_G=1.5Ω, T_{vj}=125ºC (3)(Fig.1) (Routine test)

Notes: (3) Counter arm: MBN2400E17D V_{GE}= ±15V
R_G are the test condition's value to define the switching characteristics not recommended value.
Please, determine the suitable R_G value after the measurement of switching waveforms
(overshoot voltage, etc.) with appliance mounted.

PACKAGE CHARACTERISTICS

Item	Symbol	Unit	Min.	Typ.	Max.	Test Conditions
Terminal Resistance	R_{CE}	mΩ	-	0.4	-	per arm, T_{vj}=25ºC
Stray inductance module	L_{SCE}	nH	-	35	-	per arm
Thermal Impedance	$R_{th(j\text{-}c)}$	K/W	-	-	0.045	Junction to case (par arm)
Contact Thermal Impedance	$R_{th(c\text{-}f)}$	K/W	-	0.008	-	Case to fin (par module)

* Please contact our representatives at order.
* For improvement, specifications are subject to change without notice.
* For actual application, please confirm this spec sheet is the newest revision.

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DEFINITION OF TEST CIRCUIT

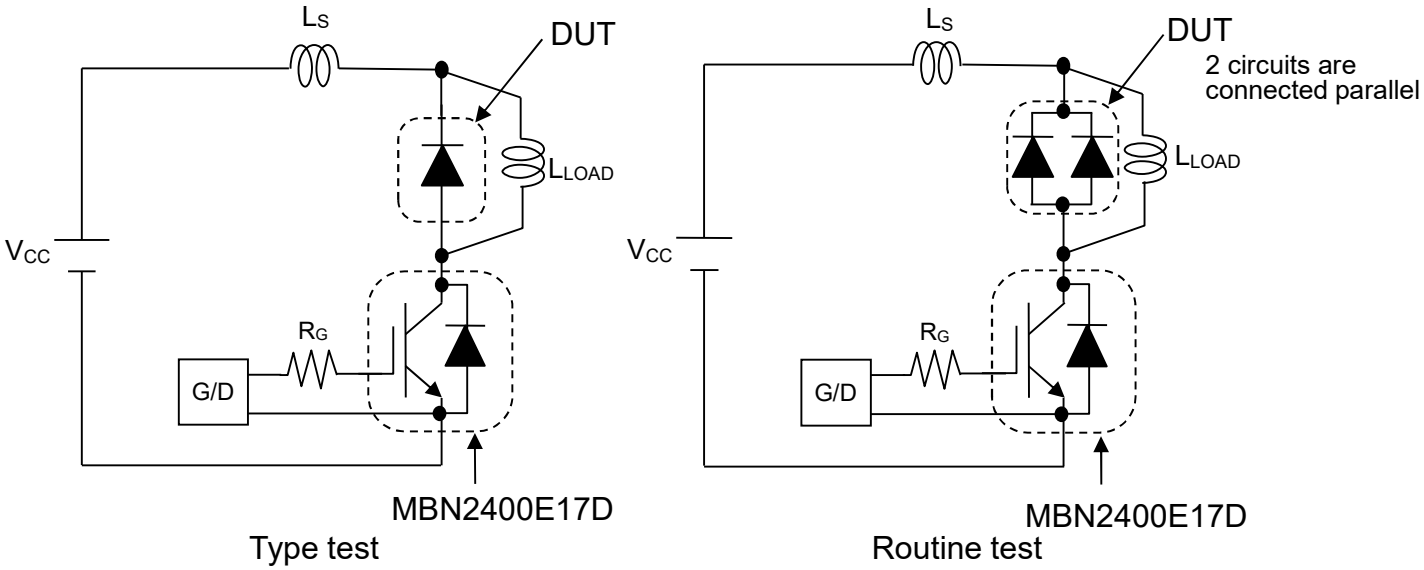
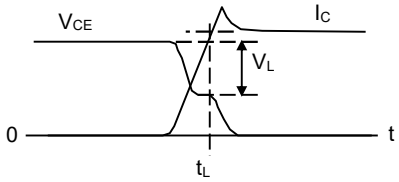
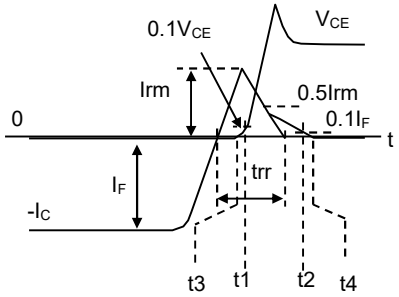


Fig.1 Switching test circuit



$$L_S = \frac{V_L}{\left(\frac{dI_C}{dt}\right)_{t=t_L}}$$

Fig.2 Definition of stray inductance



$$E_{rr(10\%)} = \int_{t1}^{t2} I_F \cdot V_{CE}\, dt$$

$$E_{rr(full)} = \int_{t3}^{t4} I_F \cdot V_{CE}\, dt$$

Fig.3 Definition of switching loss

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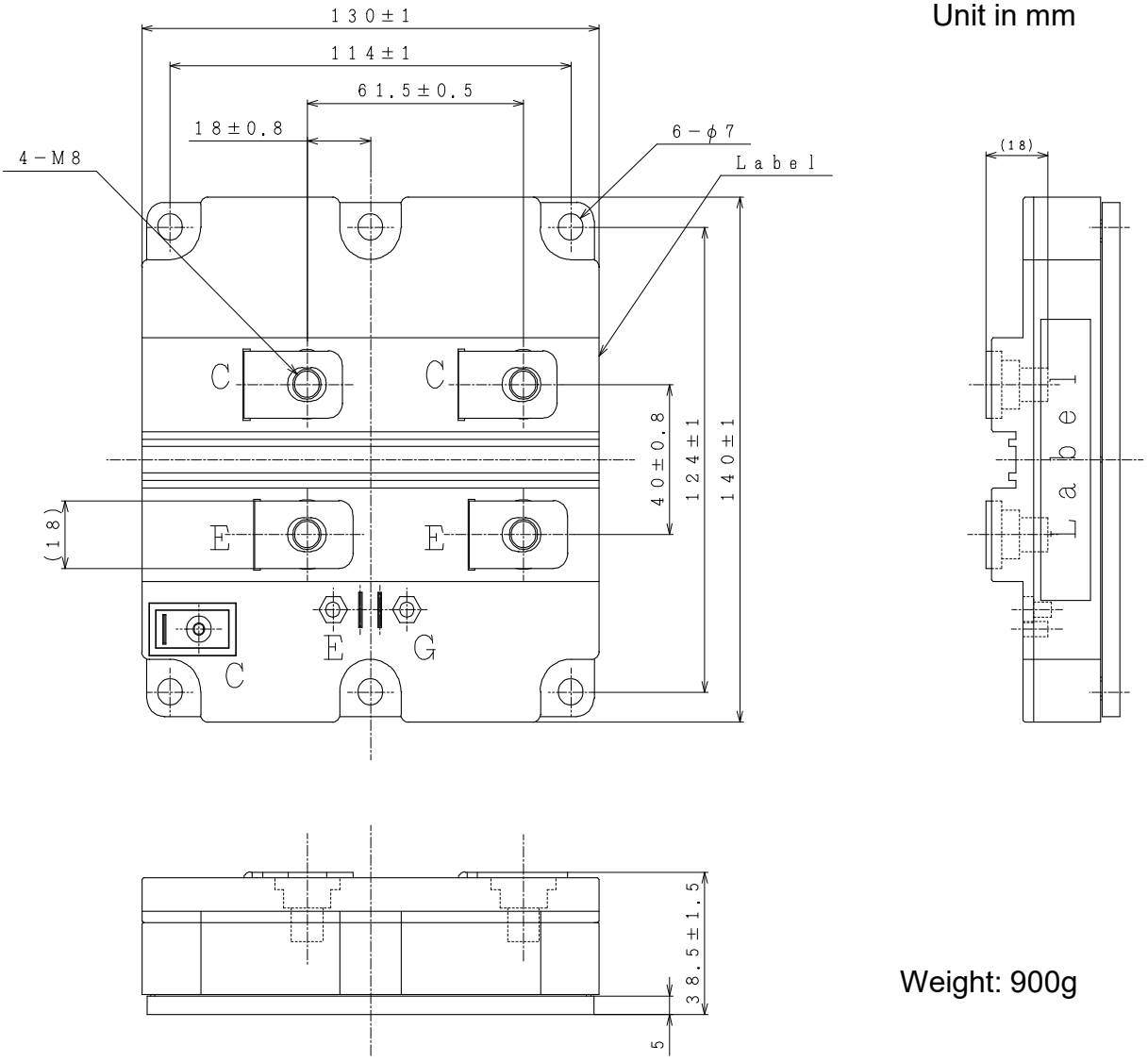
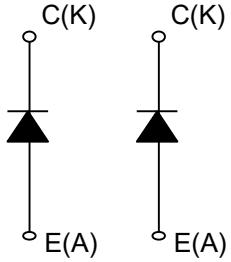
OUTLINE DRAWING

Unit in mm



Weight: 900g

CIRCUIT DIAGRAM



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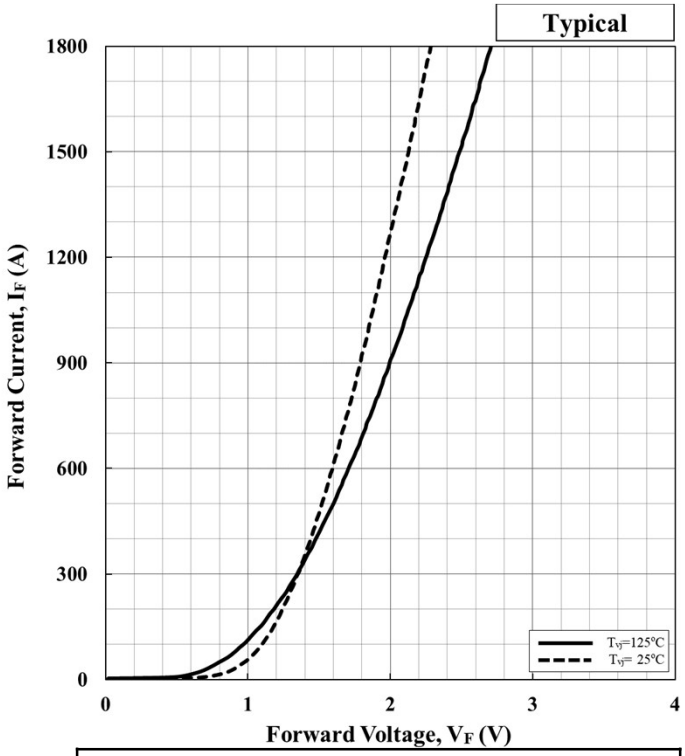
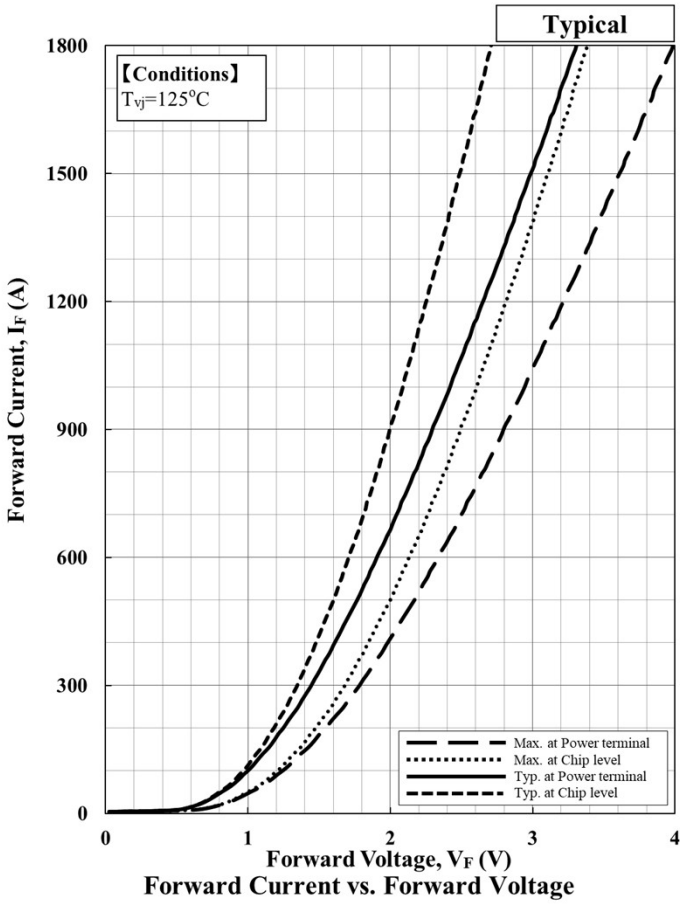


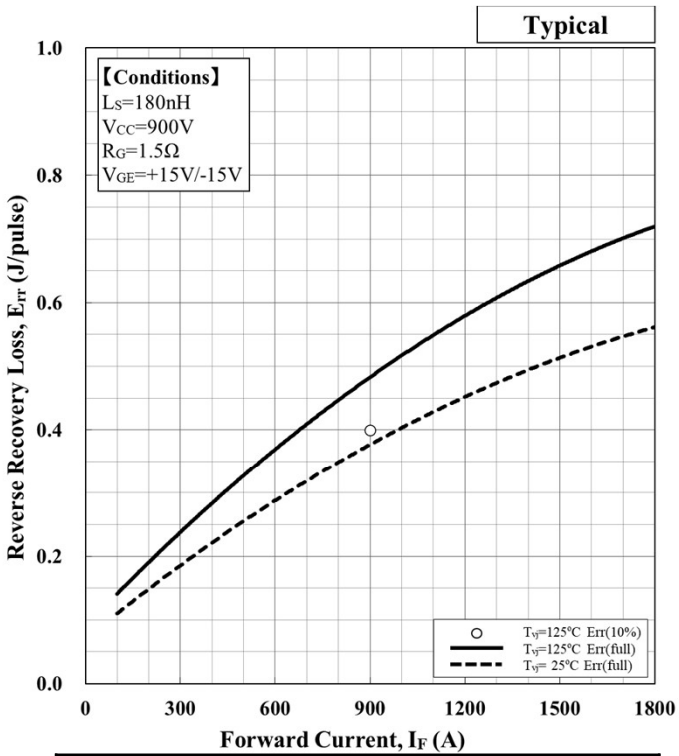
$V_F[V] = a_3 \cdot |I_F|^3 + a_2 \cdot |I_F|^2 + a_1 \cdot |I_F| + a_0$

Temp.[°C]	a_3	a_2	a_1	a_0
25	1.30E-10	-5.52E-07	1.29E-03	9.92E-01
125	1.91E-10	-8.28E-07	1.92E-03	8.12E-01

Forward Current vs. Forward Voltage (at chip level)



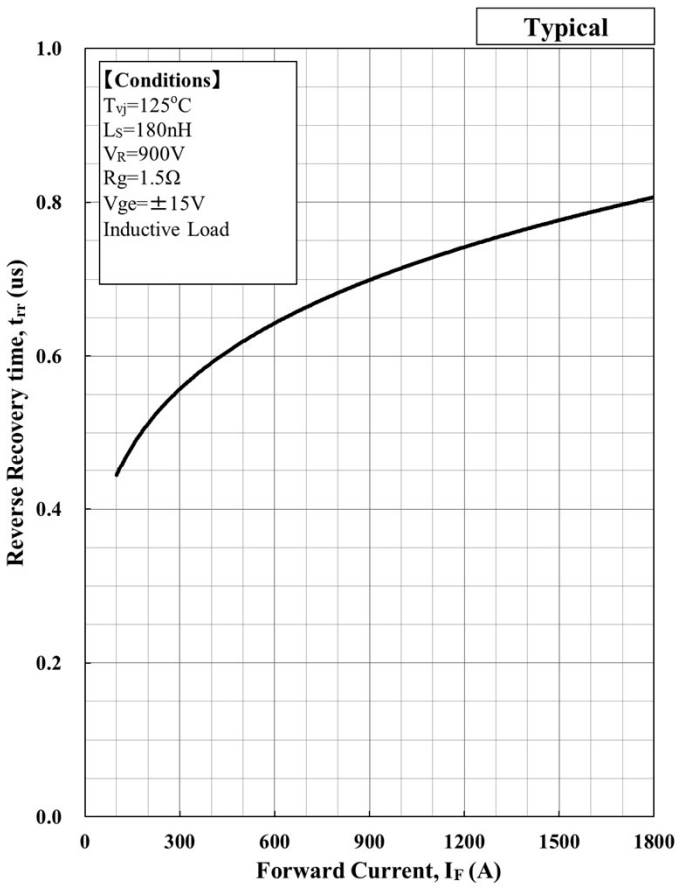
Forward Current vs. Forward Voltage



$E\ [J] = a_3 \cdot |I_F|^3 + a_2 \cdot |I_F|^2 + a_1 \cdot |I_F| + a_0$

Temp.[°C]	a_3	a_2	a_1	a_0
25	-	-7.54E-08	4.09E-04	6.98E-02
125	-	-9.67E-08	5.24E-04	8.95E-02

Recovery loss vs. Forward current



Reverse Recovery time vs. Forward Current

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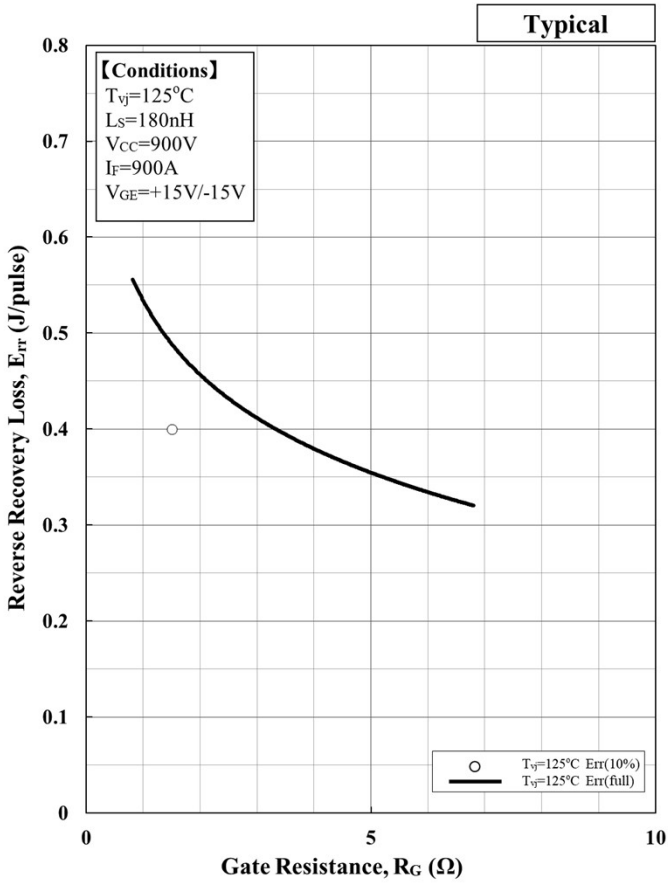
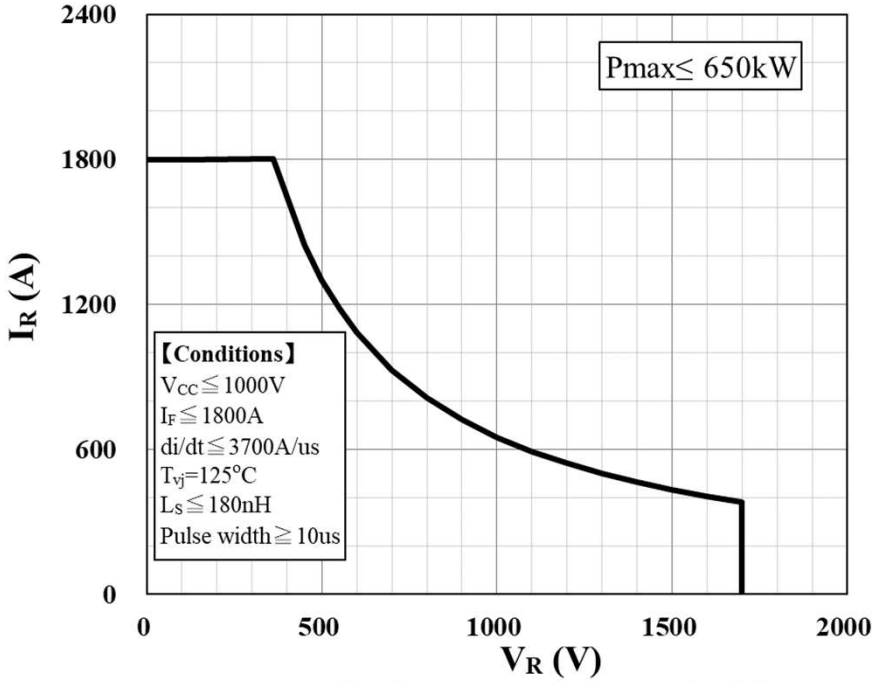
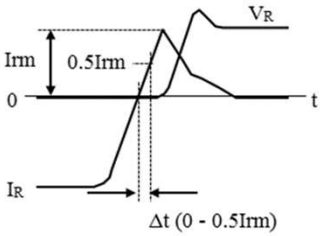


Reverse Recovery loss vs. Gate Resistance





$$di/dt = \frac{0.5Irm}{\Delta t}$$

Definition of Recovery di/dt

(Defined at power terminals)

Reverse Recovery Safe Operation Area (RRSOA)

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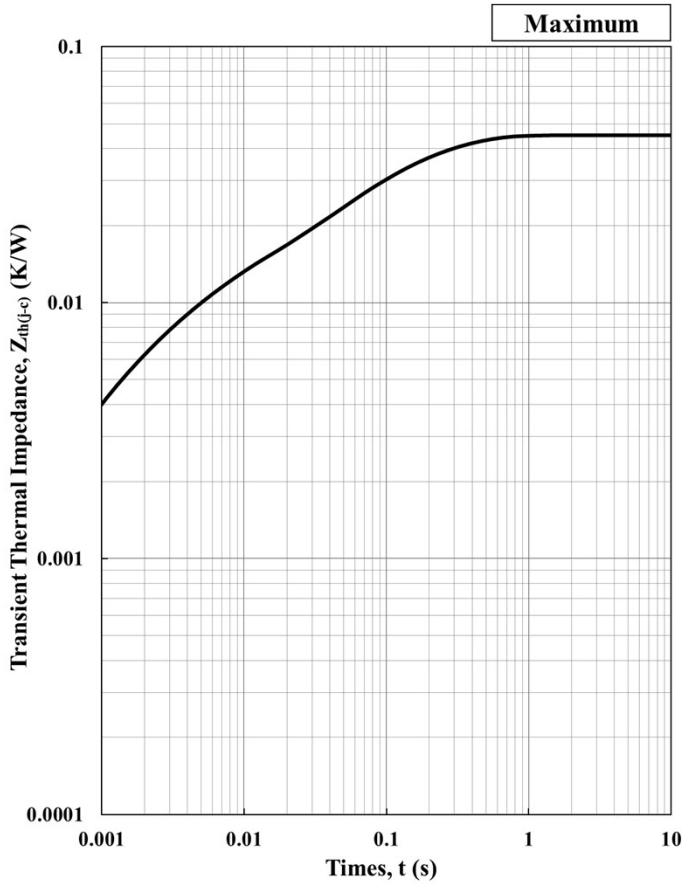

Transient Thermal Impedance Curve

Foster model lumped circuit constant

n	1	2	3	4	Unit
R th, Diode [n]	2.01E-02	1.44E-02	7.53E-03	2.97E-03	[K/W]
C th, Diode [n]	1.05E+01	3.65E+00	5.56E-01	2.88E-01	[J/K]

Cauer model lumped circuit constant

n	1	2	3	4	Unit
R th, Diode [n]	7.07E-03	7.38E-03	2.07E-02	9.85E-03	[K/W]
C th, Diode [n]	1.77E-01	5.01E-01	2.50E+00	1.68E+01	[J/K]

Material declaration

Please note the following materials are contained in the product,
in order to keep characteristic and reliability level.

Material	Contained part
Lead (Pb) and its compounds	Solder

MDM900E17D

Minebea POWER SEMICONDUCTORS

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6. This specification is a material for component selection, which describes specifications of power semiconductor devices (hereinafter referred to as products), characteristic charts, and external dimension drawings.
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8. For handling other than described in this manual, follow the handling instructions (IGBT-HI-00002).

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