IGBT MODULE Spec.No.IGBT-SP-09025 R8 P 1

MBN500H65E2

Silicon N-channel IGBT 6500V E2 version

FEATURES

- * Soft switching behavior & low conduction loss: Soft low-injection punch-through High conductivity IGBT.
- * Low driving power due to low input capacitance MOS gate.
- * Low noise recovery: Ultra soft fast recovery diode.
- * High thermal fatigue durability:

(delta Tc=70K, N>30,000cycles)

AISiC base-plate/AIN substrate

ABSOLUTE MAXIMUM RATINGS (Tc=25°C)

Item		Symbol	Unit	MBN500H65E2
	T _{vi} =125°C			6,500
Collector Emitter Voltage	T _{vi} =25°C	V _{CES}	V	6,500
· ·	T _{vi} =-40°C			6,000
Gate Emitter Voltage			V	±20
Collector Current DC		Ic	۸	500
Collector Current	1ms	I _{CRM}	A	1,000
Forward Current	DC	I _F	^	500
Forward Current	1ms	I _{FRM}	A	1,000
Operating Junction Tempe	rature	T _{vi op}	°C	-40 ~ +125
Storage Temperature		T _{stg}	°C	-50 ~ +125
Isolation Voltage		V _{ISO}	V _{RMS}	10,200(AC 1 minute)
Sorow Torquo	Terminals (M4/M8)	-	N·m	2/10 (1)
Screw Torque	Mounting (M6)	-	111-111	6 (2)

Notes: (1) Recommended Value 1.8±0.2/9±1N⋅m

(2) Recommended Value 5.5±0.5N·m

ELECTRICAL CHARACTERISTICS

Item	Symbol	Unit	Min.	Тур.	Max.	Test Conditions
Collector Emitter Cut-Off Current	I _{CES}	mA	-	•	17	V _{CE} =6,500V, V _{GE} =0V, T _{vj} =25°C
	ICES		-	17	67	$V_{CE}=6,500V, V_{GE}=0V, T_{vj}=125^{\circ}C$
Gate Emitter Leakage Current	I _{GES}	nA	-500	-	+500	$V_{GE}=\pm 20V, V_{CE}=0V, T_{vj}=25^{\circ}C$
Collector Emitter Saturation Voltage	V _{CEsat}	V	-	3.2	-	I _C =500A, V _{GE} =15V, T _{vj} =25°C
- Concetor Emitter Saturation Voltage	V CESat	-	3.4	4.3	5.2	I _C =500A, V _{GE} =15V, T _{vj} =125°C
Gate Emitter Threshold Voltage	$V_{GE(th)}$	V	5.8	6.3	6.8	$V_{CE}=10V$, $I_{C}=500$ mA, $T_{Vj}=25$ °C
Input Capacitance	Cies	nF	-	87	-	$V_{CE}=10V, V_{GE}=0V, f=100kHz, T_{vj}=25^{\circ}C$
Internal Gate Resistance	R _{G(int)}	Ω	-	1.1	-	$V_{CE}=10V$, $V_{GE}=0V$, $f=100kHz$, $T_{vj}=25^{\circ}C$
Turn On Delay Time	t _{d(on)}		-	0.7	-	V_{CC} =3,600V, I_{C} =500A
Rise Time	t _r		2.2	3.2	4.8	L _S =210nH
Turn Off Delay Time	t _{d(off)}	μS	-	3.3	-	$R_G=12\Omega$ (3)
Fall Time	t _f		2.2	3.1	4.7	$V_{GE}=\pm 15V, T_{vj}=125^{\circ}C$
Forward Voltage Drop	VF	V	-	3.6	-	$I_F=500A$, $V_{GE}=0V$, $T_{vj}=25$ °C
Forward Voltage Drop	VF	V	3.5	3.9	4.4	$I_F=500A$, $V_{GE}=0V$, $T_{vj}=125$ °C
Reverse Recovery Time	t _{rr}	μS	-	0.8	1.6	V _{CC} =3,600V, I _F =500A, L _S =200nH T _{Vi} =125°C
Turn On Loss	E _{on(10%)}	J/P	-	3.3	4.3	
Tulli Oli Loss	E _{on(full)}	3/1	-	3.7	-	V _{CC} =3,600V, I _C =500A, L _S =210nH
Turn Off Loss	E _{off(10%)}	J/P	-	2.6	3.4	$R_{G}=12\Omega$ (3)
Tulli Oli Loss	E _{off(full)}	J/F	-	2.8	-	$V_{GE}=\pm 15V$, $T_{vi}=125^{\circ}C$
Reverse Recovery Loss	E _{rr(10%)}	J/P	-	1.4	1.8	VGE-113V, 1Vj-123 C
Neverse Necovery Loss	E _{rr(full)}	J/F	-	1.5	-	
Short Circuit Pulse Width	t _{sc}	118	10	-		V _{CC} =4,500V, Ls=210nH
Short Gircuit i dise Width	LSC	μS	10	-	_	$R_G(on/off)=12/120\Omega$, $V_{GF}=\pm 15V$, $T_{vj}=25^{\circ}C$
Partial discharge extinction voltage	V _e	V_{RMS}	5,100	-	-	f=50Hz, Q _{PD} ≤10pC(acc. to IEC 61287)

Notes: (3) R_G value is a test condition value for evaluation, not recommended value. Please, determine the suitable R_G value by measuring switching behaviors.

- * Please contact our representatives at order.
- * For improvement, specifications are subject to change without notice.
- * For actual application, please confirm this spec sheet is the newest revision.
- * ELECTRICAL CHARACTERISTIC items shown in above table are according to IEC 60747-2 and IEC 60747-9.

THERMAL CHARACTERISTICS

Item		Symbol	Unit	Min.	Тур.	Max.	Test Conditions
Thormal Impadance	IGBT	R _{th(j-c)}	K/M	-	-	0.0135	lunation to appo
Thermal Impedance	FWD	R _{th(j-c)}	K/W	-	-	0.027	Junction to case
Contact Thermal Impedance		R _{th(c-f)}	K/W	-	0.007	-	Case to fin (λ grease = 1W/(m·K) heat-sink flatness \leq 50 μ m)

MODULE MECHANICAL CHARACTERISTICS

Item		Unit	Characteristics	Conditions
Weight		g	1,050	
Stray inductance in module	LS(CM-EM)	nH	21	Collector-main to Emitter-main
Comparative Tracking Index	(CTI)	-	600	
Module base plate Material		-	Al-SiC	
Baseplate Thickness		mm	5	
Insulation plate Material		-	AI N	
Terminal Surface treatment		-	Ni plating	
Case Material		-	Poly-Phenylene Sulfide	
Fire and Smoke Category		-	I2 / F3	NFF 16-102

DEFINITION OF TEST CIRCUIT

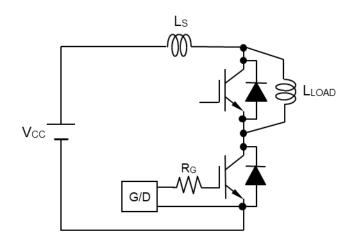


Fig.1 Switching test circuit

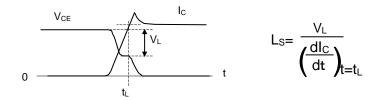


Fig.2 Definition of stray inductance

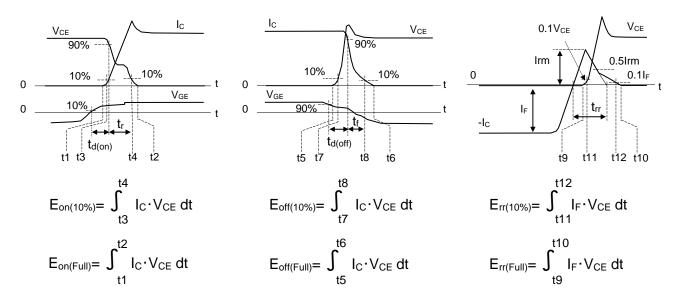
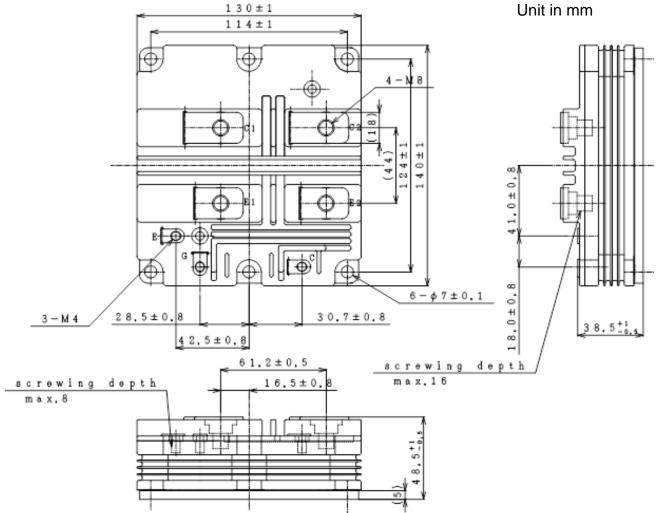


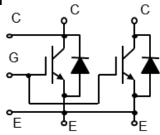
Fig.3 Definition of switching loss

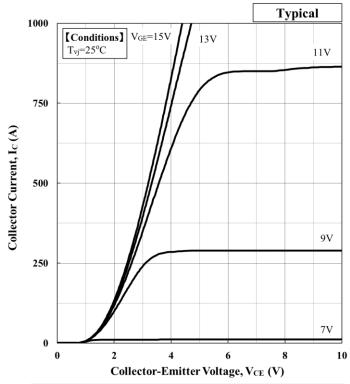
OUTLINE DRAWING



Weight: 1,050g

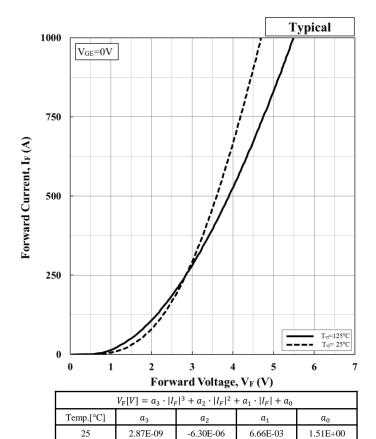
CIRCUIT DIAGRAM





$V_{\text{CE}}(sat)[V] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$							
Temp.[°C]	V _{GE} [V]	a_3	a_2	a_1	a_0		
25	15	1.87E-09	-4.16E-06	5.37E-03	1.33E+00		

Collector Current vs. Collector Emitter Voltage



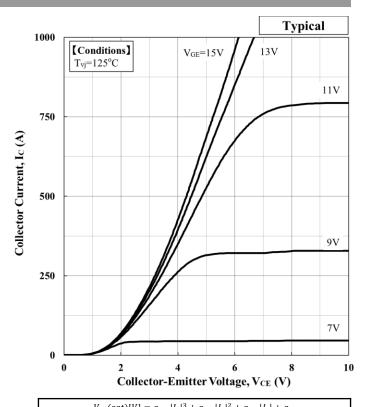
-6.36E-06 Forward Voltage of free-wheeling diode

125

2.74E-09

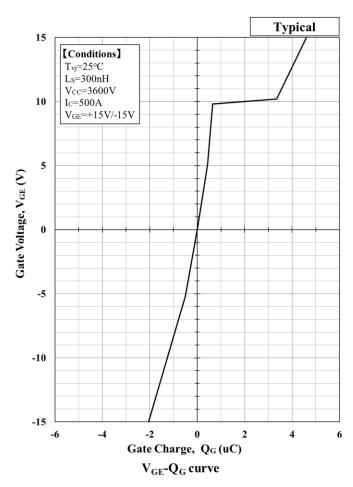
1.19E+00

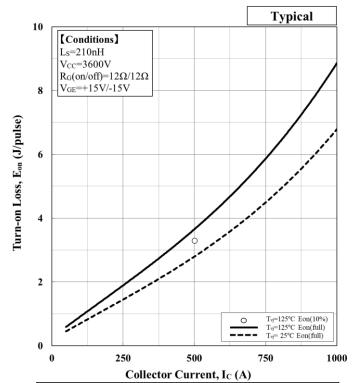
7.95E-03



$V_{CE}(Sat)[V] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$							
Temp.[°C]	V _{GE} [V]	a_3	a_2	a_1	a_0		
125	15	3.06E-09	-6.49E-06	8.10E-03	1.50E+00		

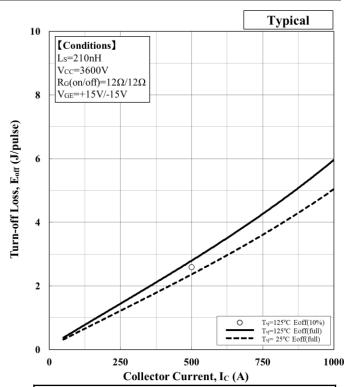
Collector Current vs. Collector Emitter Voltage





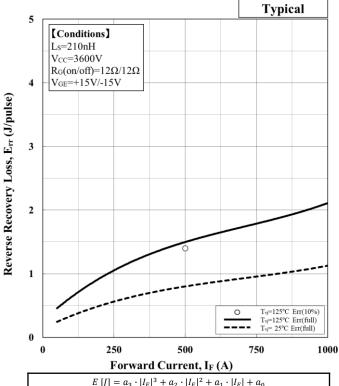
$E[J] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$						
Temp.[°C]	a_3	a_2	a_1	a_0		
25	2.70E-09	-1.27E-06	5.16E-03	1.98E-01		
125	3.52E-09	-1.65E-06	6.73E-03	2.59E-01		

Turn-on loss vs. Collector current



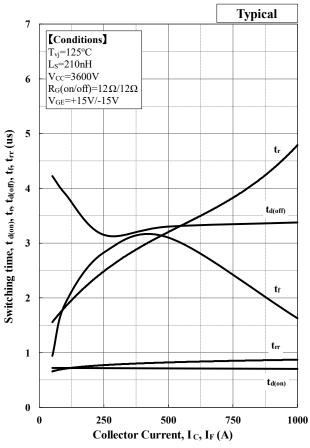
	$E[J] = a_3 \cdot I_c ^3 + a_2 \cdot I_c ^2 + a_1 \cdot I_c + a_0$							
Temp.[°C]	a_3	a_2	a_1	a_0				
25	9.59E-10	-6.32E-07	4.63E-03	8.96E-02				
125	1.13E-09	-7.47E-07	5.47E-03	1.06E-01				

Turn-off loss vs. Collector current

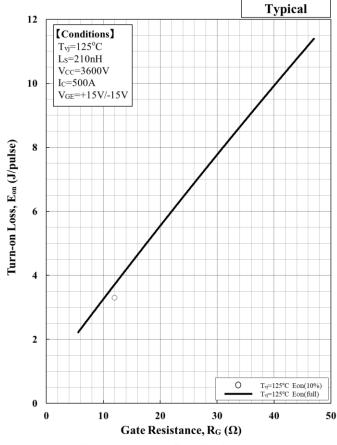


, - < ,							
$E[J] = a_3 \cdot I_F ^3 + a_2 \cdot I_F ^2 + a_1 \cdot I_F + a_0$							
Temp.[°C]	a_3	a_2	a_1	a_0			
25	1.08E-09	-2.28E-06	2.19E-03	1.40E-01			
125	2.02E-09	-4.27E-06	4.11E-03	2.62E-01			

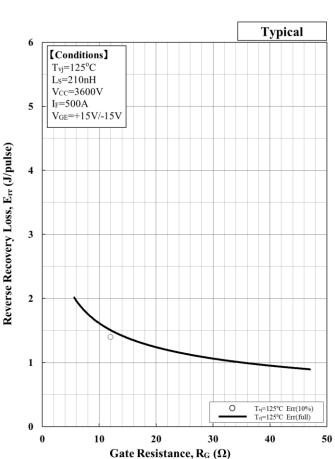
Recovery loss vs. Forward current



Switching time vs. Collector Current



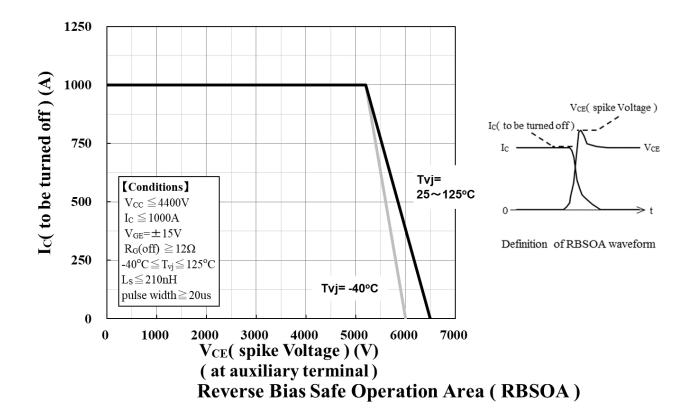
Turn-on loss vs. Gate Resistance

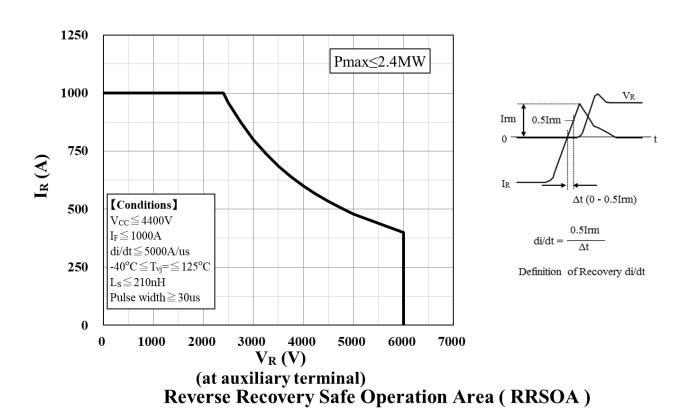


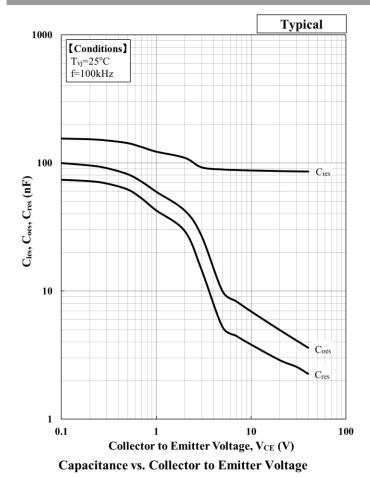
Reverse Recovery loss vs. Gate Resistance

Typical [Conditions] $T_{vj}=125^{\circ}C$ L_S=210nH V_{CC}=3600V I_C=500A $V_{GE} = +15V/-15V$ Turn-off Loss, Eoff (J/pulse) 3 2 0 T_{vj}=125°C Eoff(10%) T_{vj}=125°C Eoff(full) 0 10 20 30 40 Gate Resistance, $R_G(\Omega)$

Turn-off loss vs. Gate Resistance







0.1 Diode

| Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | Diode | D

Foster model lumped circuit constant

n	1	2	3	4	Unit
R th, IGBT [n]	8.36E-03	2.59E-03	2.43E-03	1.04E-04	[K/W]
C th, IGBT [n]	1.97E+01	1.12E+01	2.89E+00	9.03E+00	[J/K]
R th, Diode [n]	1.67E-02	5.25E-03	4.81E-03	2.13E-04	[K/W]
C th, Diode [n]	9.85E+00	5.51E+00	1.46E+00	4.42E+00	[J/K]

Cauer model lumped circuit constant

n	1	2	3	4	Unit
R th, IGBT [n]	2.09E-03	2.74E-03	4.26E-03	4.40E-03	[K/W]
C th, IGBT [n]	1.68E+00	9.76E-01	8.10E+00	2.22E+01	[J/K]
R th, Diode [n]	4.14E-03	5.52E-03	8.50E-03	8.80E-03	[K/W]
C th, Diode [n]	8.37E-01	4.91E-01	4.02E+00	1.11E+01	[J/K]

Material declaration

Please note the following materials are contained in the product, in order to keep characteristic and reliability level.

Material	Contained part
Lead (Pb) and its compounds	Solder

Minebea POWER SEMICONDUCTORS

Notices

- 1. Since mishandling of semiconductor devices may cause malfunctions, please be sure to read "Precautions for Safe Use and Notices" in the individual brochure before use.
- 2. When designing an electronic circuit using semiconductor devices, please do not exceed the absolute maximum rating specified for the device under any external fluctuations. And for pulse applications, please also do not exceed the "Safe Operating Area (SOA)".
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- 5. A semi-processed article is done now using solder which contains lead inside the semiconductor devices. There is possibility of the regulation substance depend on the applied models, so please check before using.
- 6. This specification is a material for component selection, which describes specifications of power semiconductor devices (hereinafter referred to as products), characteristic charts, and external dimension drawings.
- 7. The information given herein, including the specifications and dimensions, is subject to change without prior notice to improve product characteristics. Before ordering, purchasers are advised to contact with Minebea power semiconductor sales department for the latest version of this data sheets
- 8. For handling other than described in this manual, follow the handling instructions (IGBT-HI-00002).

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Minebea POWER SEMICONDUCTORS

■ Usage I

- MPSD warrants that the MPSD products have the specified performance according to the respective specifications at the time of its sale. Testing and other quality control techniques of the MPSD products by MPSD are utilized to the extent MPSD needs to meet the specifications described in this document. Not every device of the MPSD products is specifically tested on all parameters, except those mandated by relevant laws and/or regulations.
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